

## SPECIFICATION AMENDMENT

**In the Section ‘BRIEF DESCRIPTION OF THE DRAWINGS’ please insert  
the following paragraph between paragraphs [0036] and [0037]:**

Fig. 3D shows a conventional destructive read and destructive write memory cell consisting of two transistors and one capacitor (2T 1C cell).

No fee is believed to be due. If, however, it is deemed to be otherwise, please deduct the appropriate fee from Deposit Account 09-0458.

Respectfully submitted,  
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